

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L12	15	I9 and (leadframe chip stack\$3 (flat or planar)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2008/12/04 14:14
L11	13	I9 and (leadframe chip stack\$3 bond\$3 (flat or planar)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2008/12/04 14:12
L10	0	I9 and (leadframe chip stack\$3 bond\$3 (flat or planar)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/12/04 14:12
L9	7218	I1 or I2 or I7 or I8	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/12/04 14:11
L8	1975	438/127.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/12/04 14:11
L7	2186	438/108.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/12/04 14:11
L6	3	I5 and (method panel leadframe).clm.	US-PGPUB; USPAT	SAME	ON	2008/12/04 13:53
L5	32	Goller Bernd.in.	US-PGPUB; USPAT	SAME	ON	2008/12/04 13:52
L2	254	257/e23.048.ccls.	US-PGPUB; USPAT; USOCR	SAME	ON	2008/12/04 13:50
L1	3235	257/686.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2008/12/04 13:50

12/ 4/ 2008 2:21:09 PM

C:\Documents and Settings\jingham\My Documents\EAST\Workspaces
\packages\ 10 529565 stacked chips with contact pillars and chip island for top
chip.wsp